

SSCN123GS6

NPN Type Digital Transistor (built-in resistors)

Features

vcc	VIN	Ю	R1	R2/R1 Typ.
50V	-5~+12V	100mA	2.2kΩ	21

> Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects. Only the on/off conditions need to be set for operation, making the device design easy.

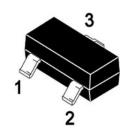
Applications

- Amplifying signal
- Electronic switch
- Oscillating circuit
- Variable resistance

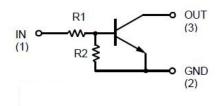
> Ordering Information

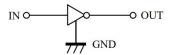
Device	Package	Shipping
SSCN123GS6	SOT-23	3000/Reel

Pin configuration

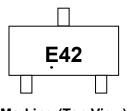


SOT-23





Circuit Diagram



Marking (Top View)



ightharpoonup Absolute Maximum Ratings(T_A=25°C unless otherwise noted)

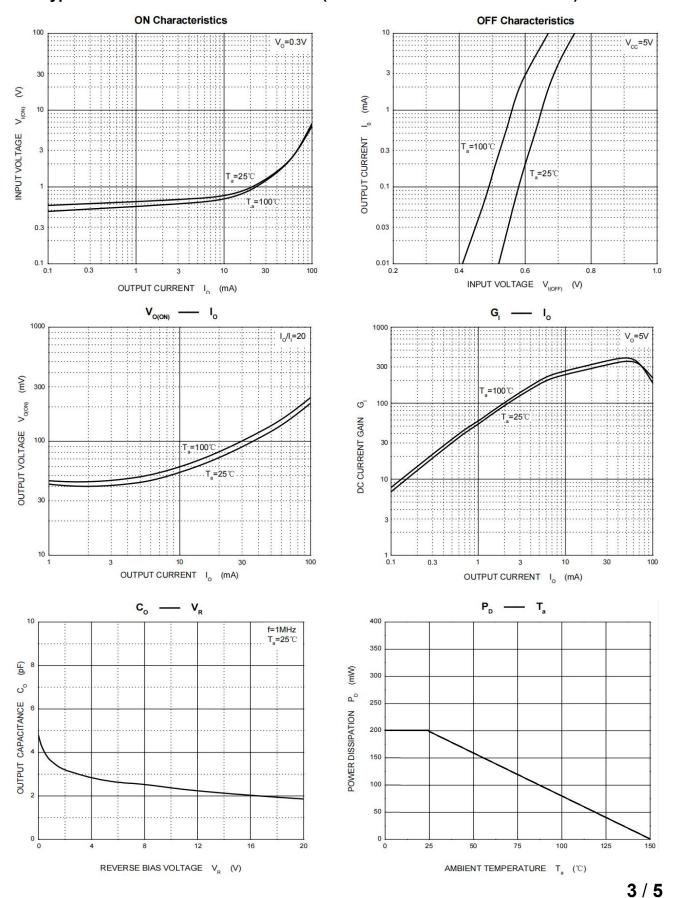
Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	50	V
Input Voltage	V _{IN}	-5 to +12	V
Output current	lo	100	mA
Power Dissipation	P _D	200	mW
Junction Temperature	TJ	-55 to 150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

➤ Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Input Valtage	V _{I(off)}	$V_{CC} = 5V, I_0 = 0.1 \text{mA}$	0.5			\ \
Input Voltage	V _{I(on)}	$V_{CC} = 0.3V$, $I_{O} = 5mA$			1.1	V
Output Voltage	V _{O(on)}	I _O /I _I = 5mA/0.25mA		0.1	0.3	V
Input Current	l _l	V _I = 5V			3.6	mA
Output Current	I _{O(off)}	V _{CC} = 50V, V _I = 0V			0.5	uA
DC Current Gain	G₁	V _O = 5V, I _O = 10mA	80			
Input Resistance	R ₁		1.54	2.2	2.86	ΚΩ
Resistance Ration	R ₂ /R ₁		17	21	26	
Transition Frequency	f⊤	V ₀ =10V,I ₀ =5mA,f=100MHz		250		MHz



\succ Typical Performance Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

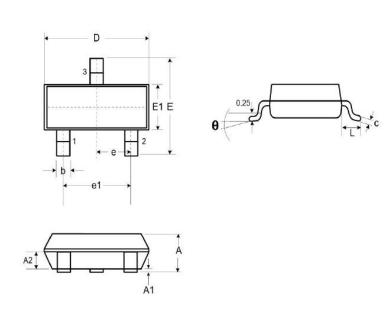




Package Information

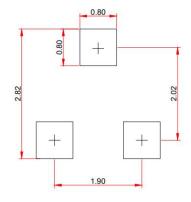
Mechanical Data

SOT-23



DIM	Millimeters				
DIM	Min.	Тур.	Max.		
Α	0.89	1	1.12		
A 1	0.01	ı	0.10		
A2	0.88	0.95	1.02		
b	0.30	-	0.51		
С	0.08	ı	0.18		
D	2.80	2.90	3.04		
E	2.10	2.37	2.64		
E1	1.20	1.30	1.40		
е	0.95				
e1	1.90				
L	0.40	0.50	0.60		
L1	0.55				
N	3				
θ	0°	-	8°		

Recommended Pad outline





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